

**IN THE CLAIMS:**

Please amend the claims as follows:

1. (Currently Amended): A liquid crystal display, comprising:

a liquid crystal polarity inversion driver determining whether a polarity of a liquid crystal is inverted and inverting the polarity of the liquid crystal in accordance with the determined result;

a first data inversion driver determining whether a number of transition of odd data having a plurality of bits is more than half of the plurality of bits of the odd data, and inverting the odd data in accordance with the determined result; and

a second data inversion driver determining whether a number of transition of even data having a plurality of bits is more than half of the plurality of bits of the even data, and inverting the even data in accordance with the determined result,

wherein the first data inversion driver includes

a first data transition part determining whether a first data transition has occurred in the odd data and outputting a first signal,

a first data inversion signal summer counting the number of the first signal and outputting a first REV signal having a high level or a low level according to the counted number of the first signal, and

a first data inversion signal output part receiving the first REV signal from the first data

inversion signal summer and outputting either the odd data or an inverted odd data according to the first REV signal, and

wherein the second data inversion driver includes

a second data transition part determining whether a second data transition has occurred in the even data and outputting a second signal,

a second data inversion signal summer counting the number of the second signal and outputting a second REV signal having a high level or a low level according to the counted number of the second signal, and

a second data inversion signal output part receiving the second REV signal from the second data inversion signal summer and outputting the even data or an inverted even data according to the second REV signal.

2-3. (Cancelled).

4. (Currently Amended): The liquid crystal display according to claim 1 [[2]], wherein the first data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current odd data with previous odd data to determine whether the first data transition has occurred in accordance with the compared result.

5. (Currently Amended): The liquid crystal display according to claim 1 [[3]], wherein the second data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current even data with previous even data to determine whether the second data transition has occurred in accordance with the compared result.

6. (Currently Amended): The liquid crystal display according to claim 1 [[2]], wherein the first data inversion signal summer includes:

an adder counting the number of the first data transition from the first data transition part;  
and

a majority detector determining whether the counted number of the first data transition is higher than a first reference value.

7. (Currently Amended): The liquid crystal display according to claim 1 [[3]], wherein the second data polarity inversion signal summer includes:

an adder counting the number the second data transition from the second data transition part; and

a majority detector determining whether the counted number of the second data transition is higher than a second reference value.

8. (Currently Amended): The liquid crystal display according to claim 1 [[2]], wherein the first data inversion signal output part includes a multiplexer receiving a first inversion signal from the first data inversion signal summer to invert the output odd data.

9. (Currently Amended): The liquid crystal display according to claim 1 [[3]], wherein the second data polarity inversion signal output part includes a multiplexer receiving a second inversion signal from the second data inversion signal summer to invert the output even data.

10. (Cancelled).

11. (Currently Amended): A method of driving a liquid crystal display having first and second data inversion drivers, the method comprising:

dividing input data into odd data and even data;

comparing current odd data with previous odd data and current even data with previous even data to determine a first data transition and a second data transition, respectively;

determining a number of first and second data transitions in the odd and even data, respectively; and

inverting the odd and even data in accordance with the determined results, respectively,

wherein inverting the odd and even data includes

inverting the odd data and outputting the inverted odd data if the number of the first data transition is more than half of a total number of the odd data bit, and outputting the odd data without an inversion if the number of the first data transition is less than or equal to the half of the total number of the odd data bit, and

inverting the even data and outputting the inverted even data if the number of the second data transition is more than half of a total number of the even data bit, and outputting the even data without an inversion if the number of the second data transition is less than or equal to the half of the total number of the even data bit.

12-13. (Cancelled).

14. (Currently Amended): The method according to claim 11 [[12]], wherein the total number of the odd data bit is 18.

15. (Cancelled).

16. (Currently Amended): A 2-port data polarity inverter for driving a liquid crystal display, comprising:

an odd data inversion driver to generate a first inversion signal to invert odd data when a

number of a first data transition is more than half of a total number of the odd data; and

an even data inversion driver to generate a second inversion signal to invert even data  
when a number of a second data transition is more than half of a total number of the even data,

wherein the odd data inversion driver includes

a first data transition part to determine whether the first data transition has occurred in the  
odd data and outputting a first signal,

a first data inversion signal summer counting the number of the first signal and outputting  
a first REV signal having a high level or a low level according to the counted number of the first  
signal, and

a first data inversion signal output part receiving the first REV signal from the first data  
inversion signal summer and outputting the odd data or an inverted odd data according to the first  
REV signal, and

wherein the second data inversion driver includes

a second data transition part to determine whether the second data transition has occurred  
in the even data and outputting a second signal,

a second data inversion signal summer counting the number of the second signal and  
outputting a second REV signal having a high level or a low level according to the counted  
number of the second signal, and

a second data inversion signal output part receiving the second REV signal from the

second data inversion signal summer and outputting the even data or an inverted even data according to the second REV signal.

17. (Cancelled).

18. (Currently Amended): The 2-port data polarity inverter according to claim 16 [[17]],

wherein the first data transition part includes first and second flip-flops and a first exclusive logical sum gate to compare current odd data with previous odd data to determine whether the first data transition has occurred in accordance with the compared result, and

wherein the second data transition part includes third and fourth flip-flops and a second exclusive logical sum gate to compare current even data with previous even data to determine whether the second data transition has occurred in accordance with the compared result.

19. (Currently Amended): The 2-port data polarity inverter according to claim 16 [[17]],

wherein the first data inversion signal summer includes a first adder counting the number of the first data transition from the first data transition part, and a first majority detector to determine whether the counted number of the first data transition is higher than a first reference value; and

wherein the second data inversion signal summer includes a second adder counting the

number of the second data transition from the second data transition part; and a second majority detector to determine whether the counted number of the second data transition is higher than a second reference value.

20. (Currently Amended): The 2-port data polarity inverter according to claim 16 [[17]],

wherein the first data inversion signal output part includes a first multiplexer receiving the first REV signal from the first data inversion signal summer to invert the odd data, and

wherein the second data inversion signal output part includes a second multiplexer receiving the second REV signal from the second data inversion signal summer to invert the even data.